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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/813,169	03/31/2004	Peter Hazucha	INTEL-0062	4484
34610	7590	08/02/2005		EXAMINER
FLESHNER & KIM, LLP P.O. BOX 221200 CHANTILLY, VA 20153			LE, DINH THANH	
			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 08/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/813,169	XU ET AL	
	Examiner	Art Unit	
	DINH T. LE	2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 31 May 2005.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-4,6-9,11-17 and 19-24 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-4,6-9,11-17 and 19-24 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: _____.

FINAL REJECTION

The rejection under 35 USC 112, second paragraph, is withdrawn in view of the amendments to the claims.

Claim Rejections

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

Claims 1-4, 6-9, 11-17 and 19-24 are rejected under 35 U.S.C. 103(a) as being obvious over Huang et al (US2004/0124715, S/N=10/331,659) in view of Sakai et al (US 4,961,643). The applied reference (US2004/0124715) has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art only under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 103(a) might be overcome by: (1) a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not an invention “by another”; (2) a showing of a date of invention for the claimed subject matter of the application which corresponds to subject matter disclosed but not claimed in the reference, prior to the effective U.S. filing date of the reference under 37 CFR 1.131; or (3) an oath or declaration under 37 CFR 1.130 stating that the application and reference are currently owned by the same party and that the inventor named in the application is the prior inventor under 35 U.S.C. 104, together with a terminal disclaimer in accordance with 37 CFR 1.321(c). This rejection might also be overcome by showing that the

reference is disqualified under 35 U.S.C. 103(c) as prior art in a rejection under 35 U.S.C. 103(a). See MPEP § 706.02(l)(1) and § 706.02(l)(2).

Huang et al discloses in Figures 2-13 a circuit comprising:

- a power grid (VCC) to distribute a voltage, see Figure 13;
- a band-pass filter portion (360) to pass signals;
- a comparator portion (210) coupled to the band-pass filter portion (360) for sensing the voltage fluctuation at approximately resonance frequency;
- an amplification portion (330) coupled between the comparator portion (210) and a current dissipation portion having a MOS transistor (220) which changes a frequency of current.

However, Huang et al does not disclose that the band-pass filter comprising an operational amplifier as combined in claims 1 and 11, and a resistor and a capacitor as combined in claims 6-9, 16-17 and 19-20 and the bandpass filter comprising a first capacitor and a second capacitor coupled between an input and an output of an inverter as recited in claim 24.

Sakai et al teaches in Figures 3 and 4b an active band pass filter circuit (29) including an operational amplifier (configured as an inverter 60) having a capacitor (64) and a resistor (62) coupled in parallel with the amplifier (60) and a series connected capacitor (61) and a resistor (63) coupled to the input of the amplifier (60) for providing an active gain.

It would have been obvious to a person having skill in the art at the time the invention was made to employ the active band pass filter circuit taught by Sakai et al in the circuit of Huang et al for the purpose of providing active gain.

With regard to claims 2-3 and 12-14, the amplification portion (330) to receive a trigger signal from the comparator potion (210) and to delay turning ON the current dissipation portion

(220) and the comparator portion (210) and the amplification portion (330) operate such that the current dissipation portion (220) is OFF when there is substantially no noise on the power grid (Vcc).

With regard to claims 8 and 19, the gain of the band-pass filter portion is based on a ratio of impedance of a first resistor (62) and a first capacitor (64) and an impedance of a second resistor (63) and a second capacitor (61) as shown on Figure 4b of Sakai et al.

Response to Applicant's Arguments

The examiner agreed with the applicant's argument that the rejection stated under "35 U.S.C. 102(b)" over Huang et al in view of Sakai et al in the last Office action is a typographical error.

The applicant argues that Huang may not be used as prior art because the Huang reference and the present invention were commonly assigned at the time of the invention to Intel Corporation. The argument is not persuasive because the Huang reference is still qualified as a prior art under 35 USC 103 (a) as stated above.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after

the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DINH T. LE whose telephone number is (571) 272-1745. The examiner can normally be reached on Monday-Friday (8AM-7PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY CALLAHAN can be reached at (571) 272-1740.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



DINH T. LE
PRIMARY EXAMINER